

ABSTRACT

1 Electrical fuses (eFuses) are applied to the task of
2 memory performance adjustment to improve upon earlier fuse
3 techniques by not requiring an additional processing step and
4 expensive equipment. Standard electrical fuse (eFuse) hardware
5 chains provide a soft test feature wherein the effect of
6 memory slow-down can be tested prior to actually programming
7 the fuses. Electrical fuses thus provide a very efficient
8 non-volatile method to match the logic-memory interface
9 through memory trimming, drastically cutting costs and cycle
10 times involved.